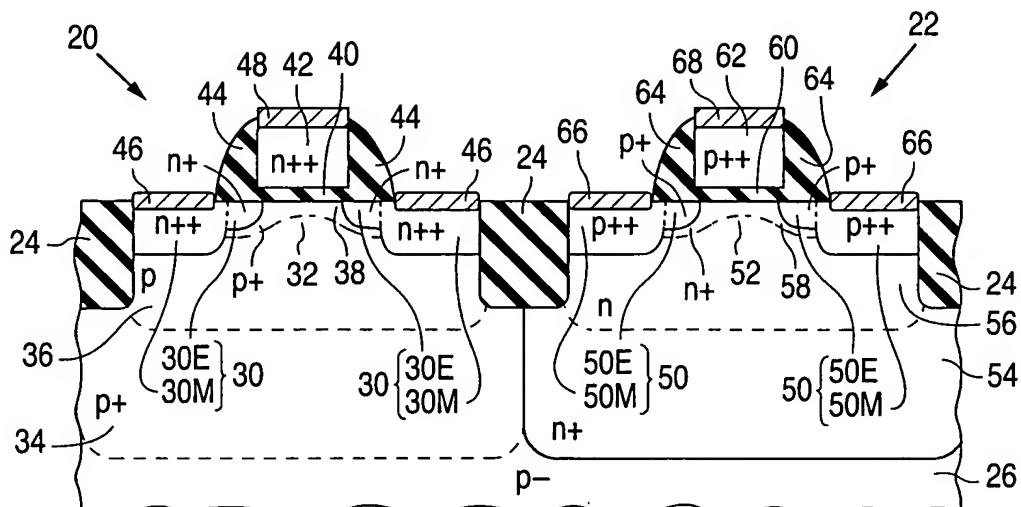
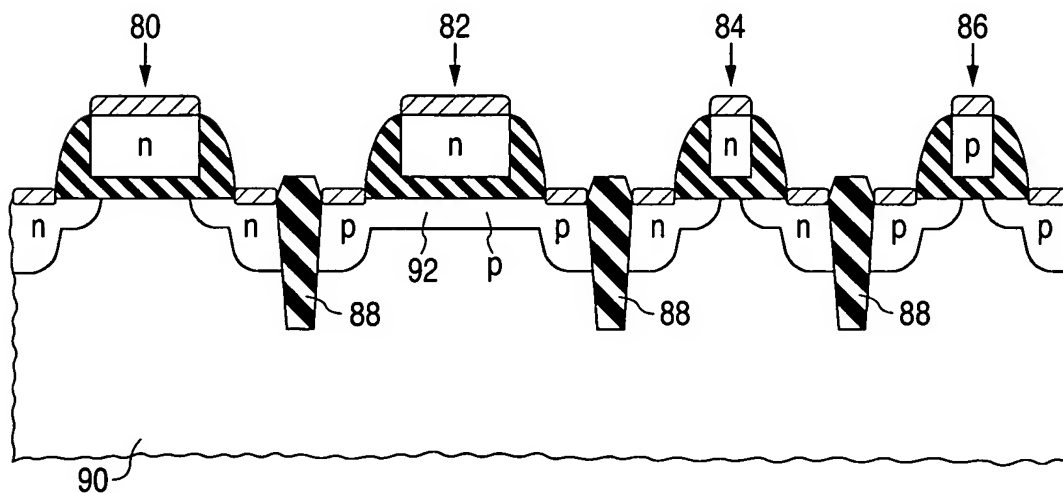


1/24

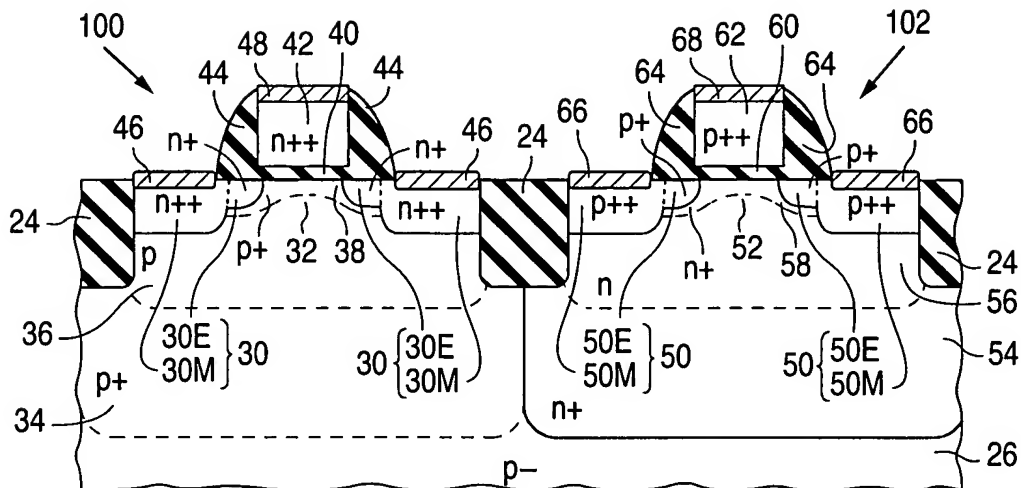
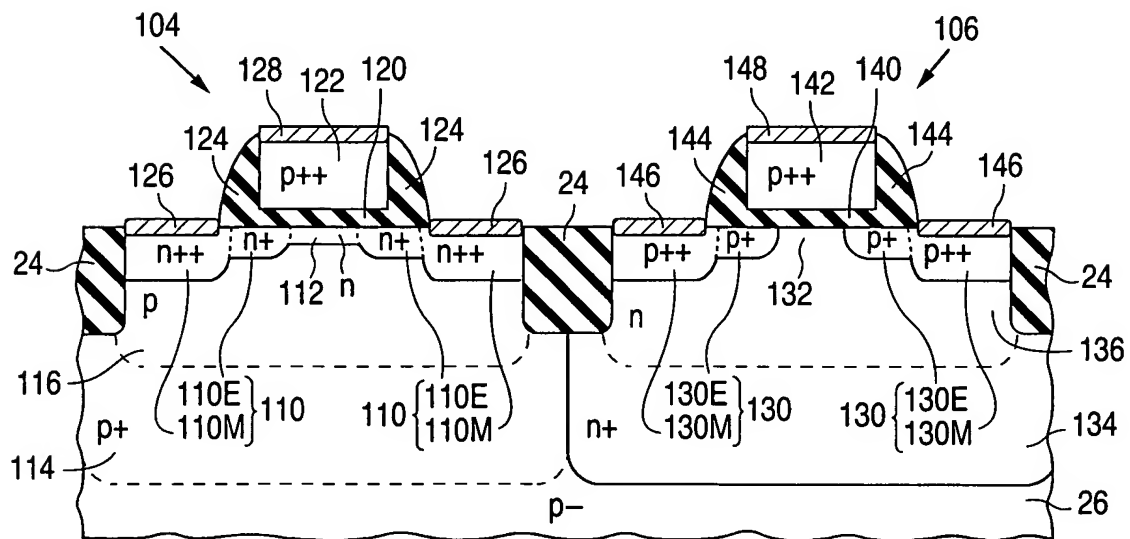


**Fig. 1**  
PRIOR ART



**Fig. 2**  
PRIOR ART

2/24

**Fig. 3.1****Fig. 3.2**

3/24

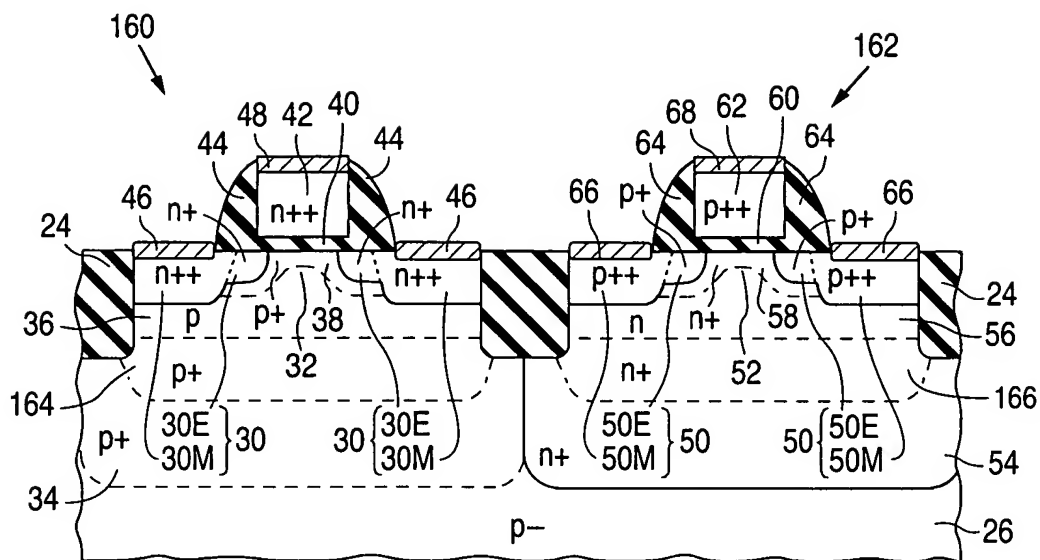


Fig. 4

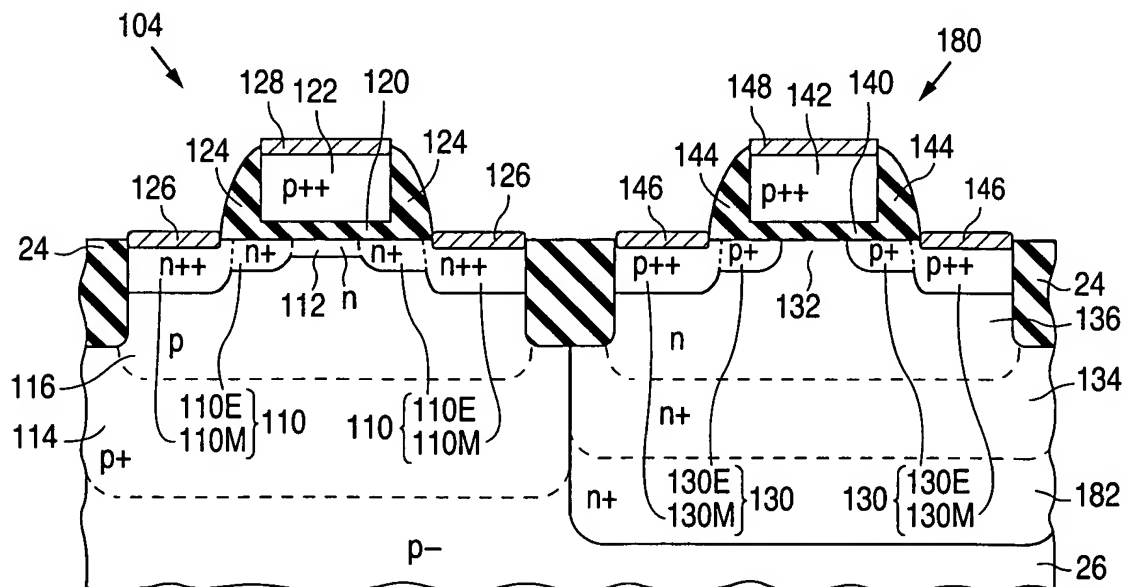
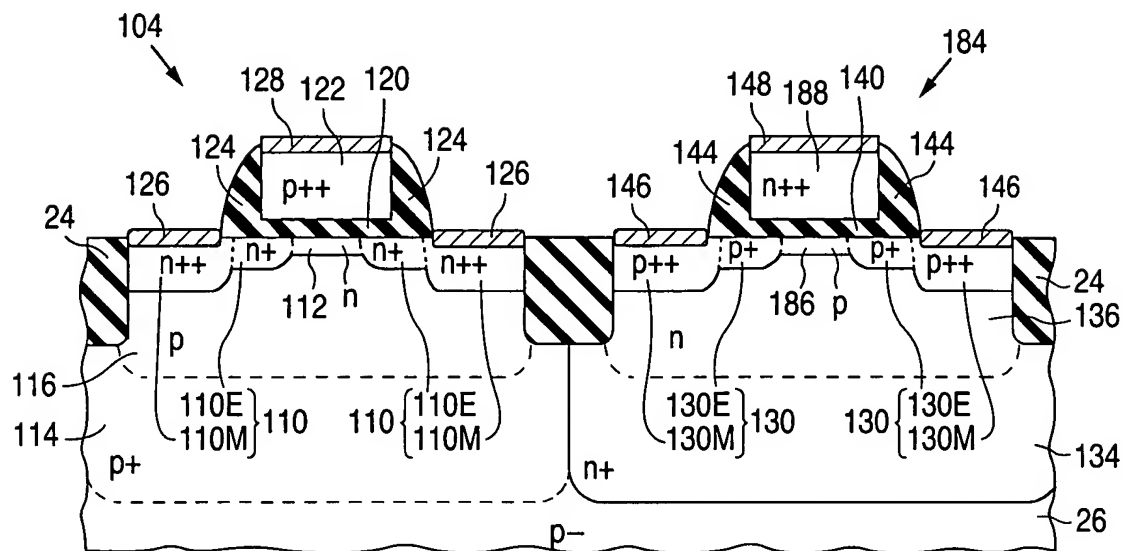
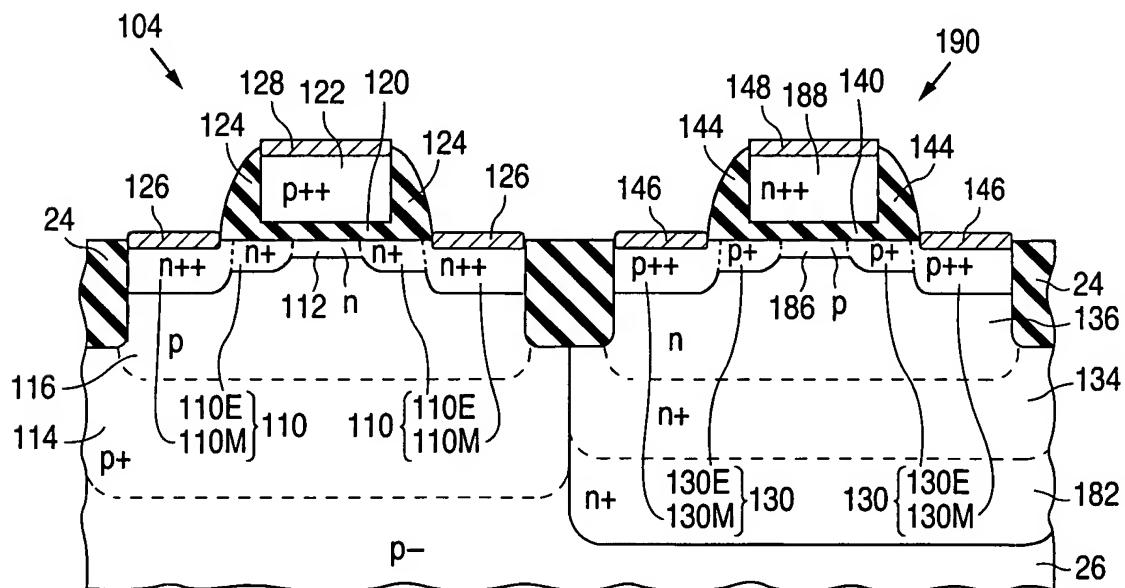


Fig. 5

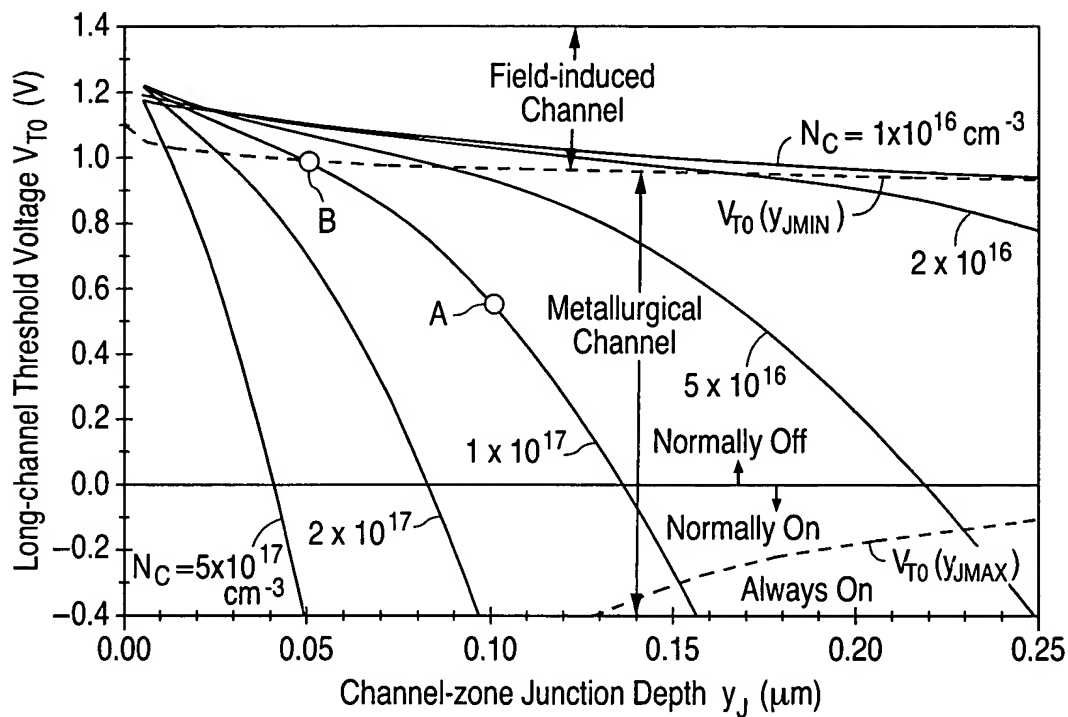
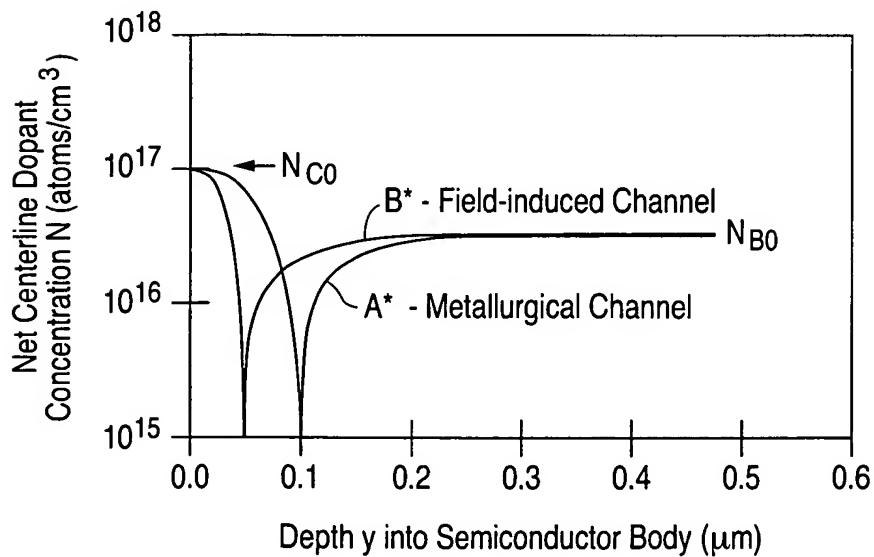


**Fig. 6**

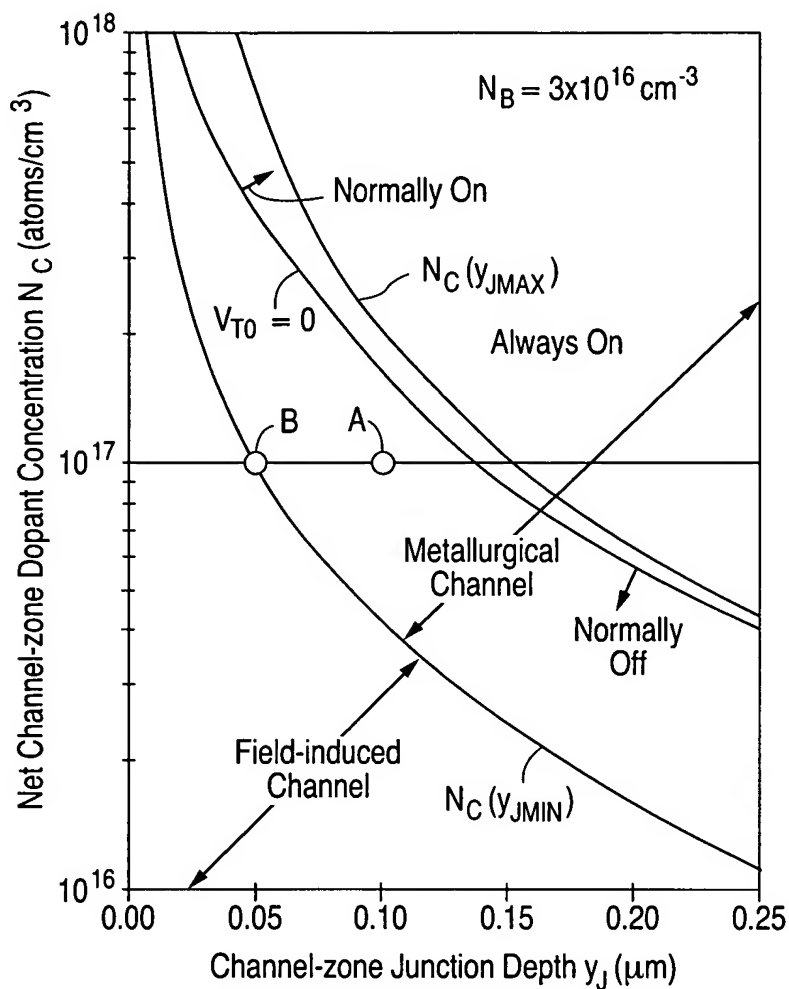
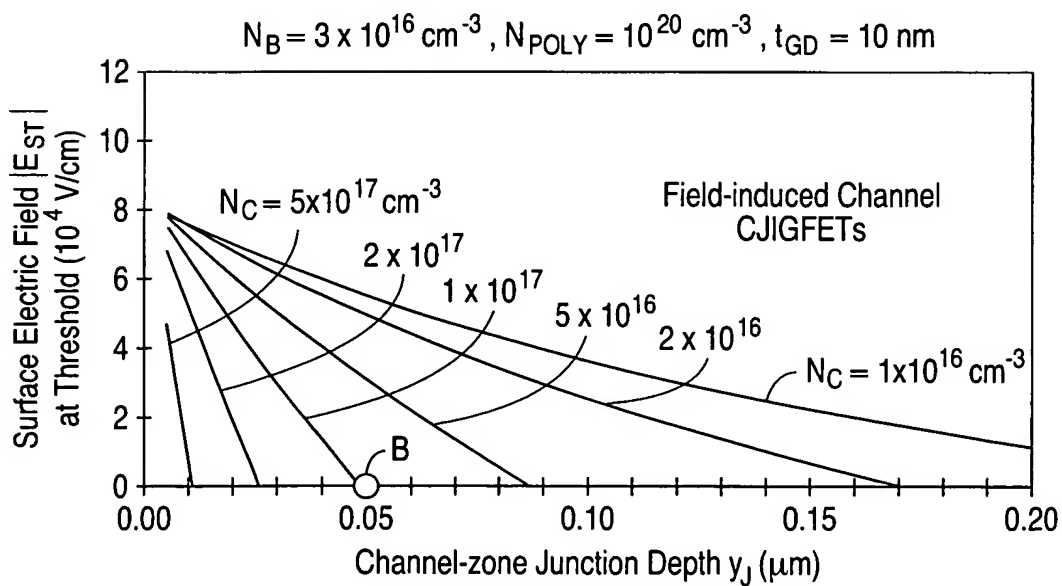


**Fig. 7**

5/24

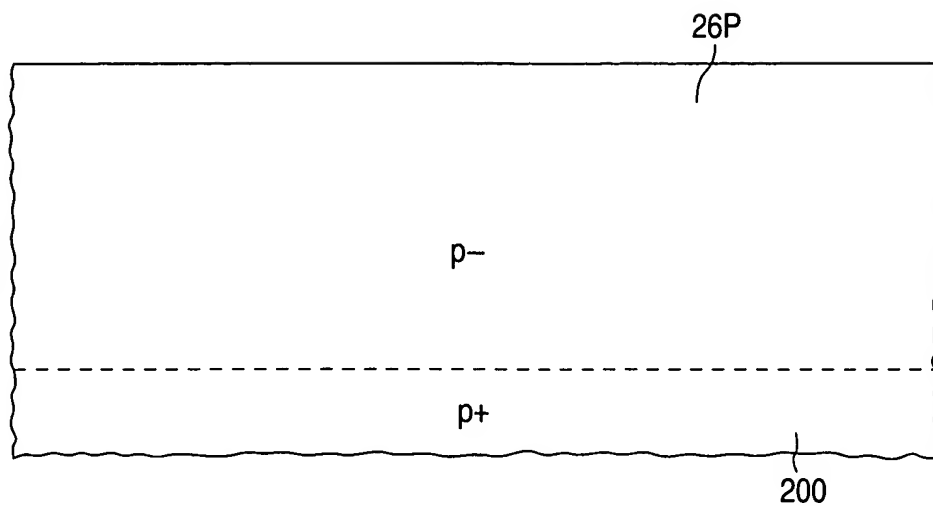
N-channel CJIGFET,  $N_B = 3 \times 10^{16} \text{ cm}^{-3}$ ,  $N_{\text{POLY}} = 10^{20} \text{ cm}^{-3}$ ,  $t_{\text{GD}} = 10 \text{ nm}$ **Fig. 8****Fig. 9**

6/24

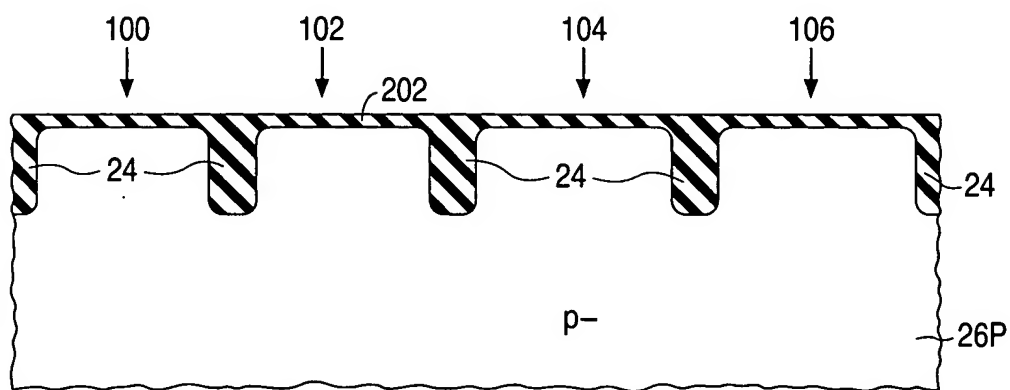
**Fig. 10****Fig. 11**

7/24

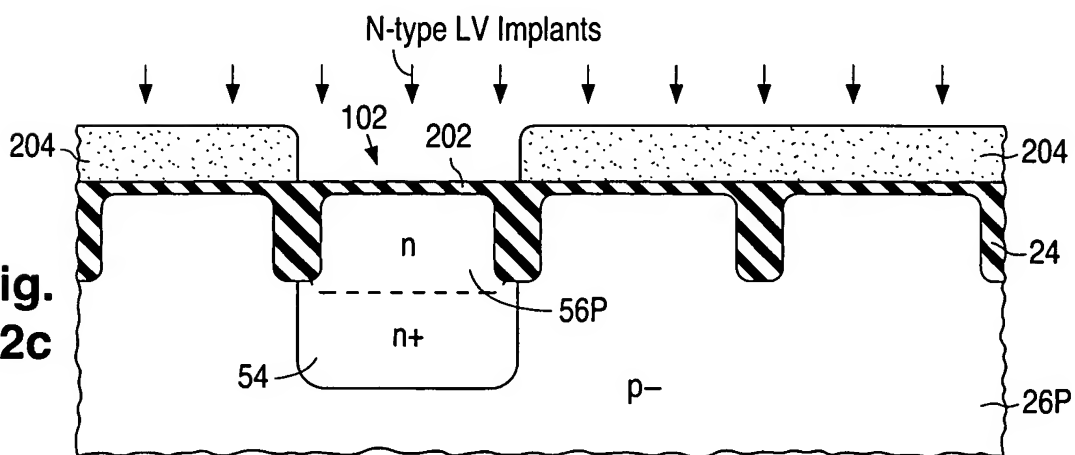
**Fig.  
12a**



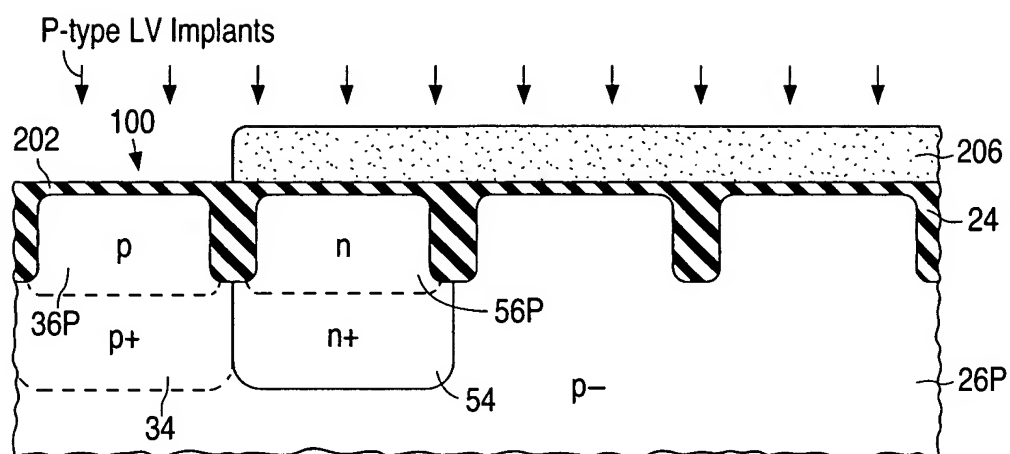
**Fig.  
12b**



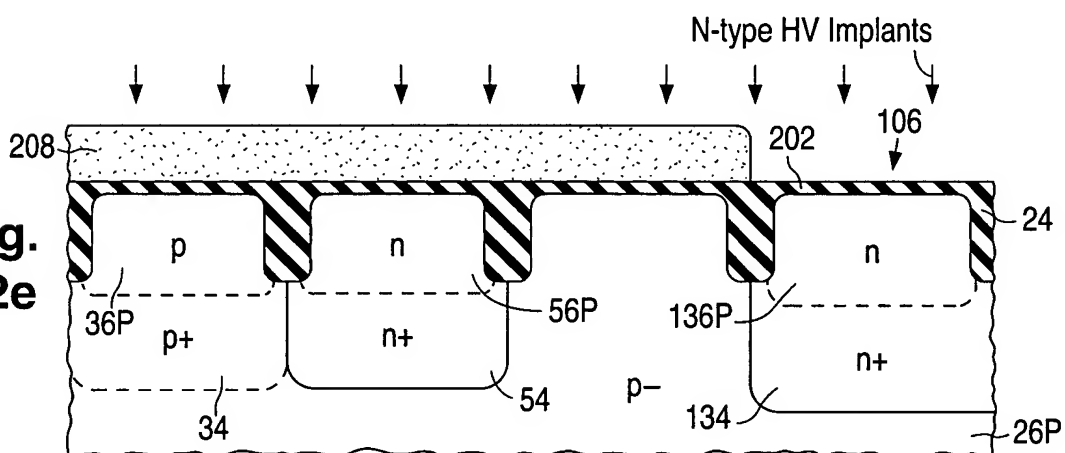
**Fig.  
12c**



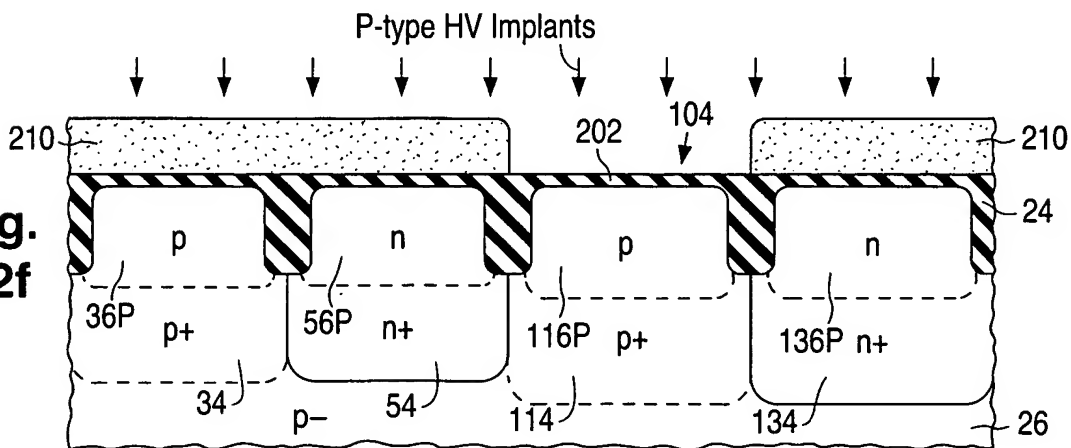
**Fig.  
12d**



**Fig.  
12e**

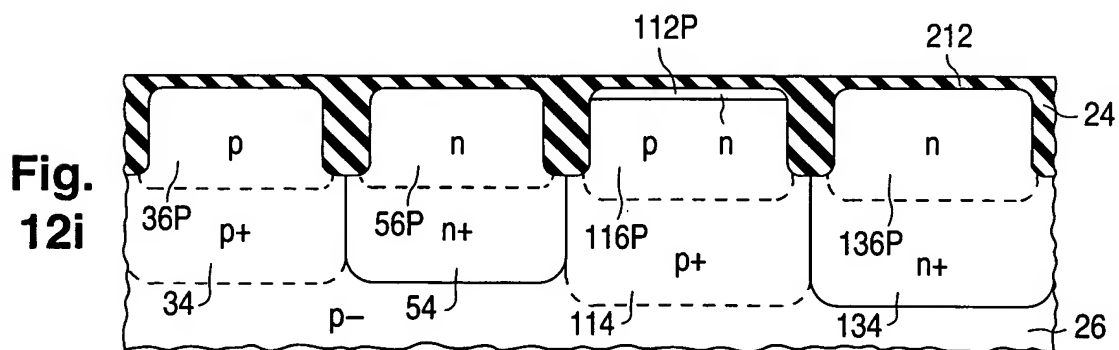
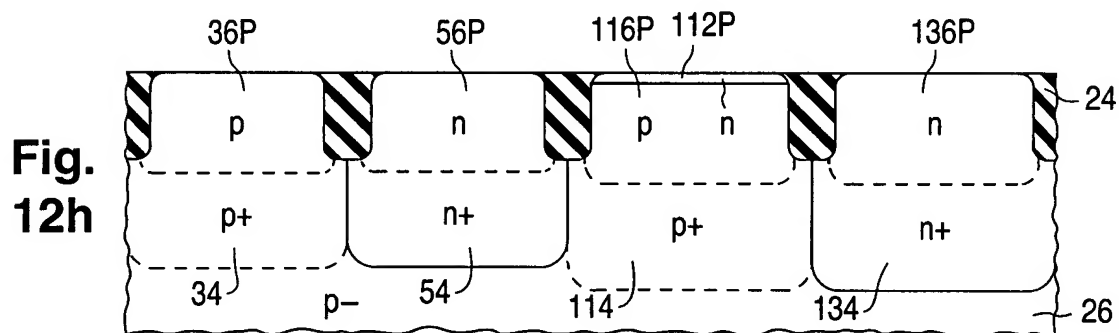
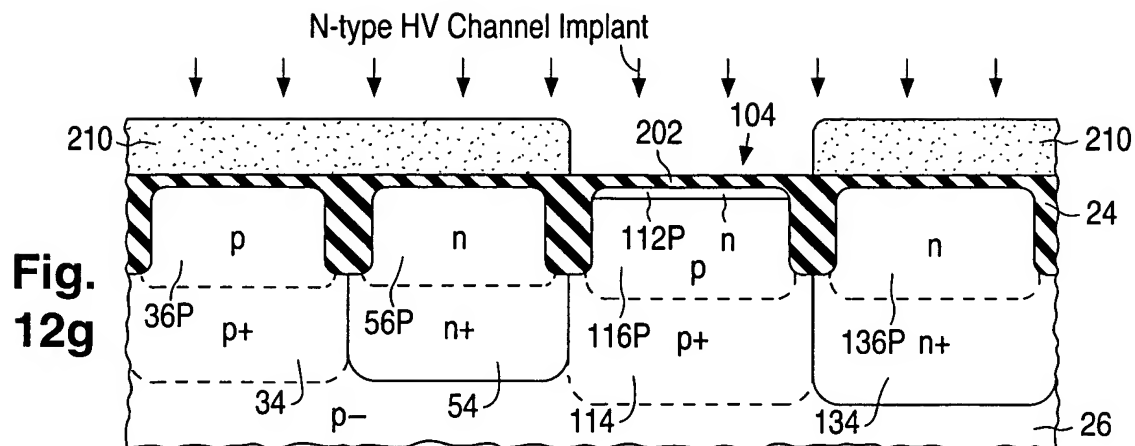


**Fig.  
12f**

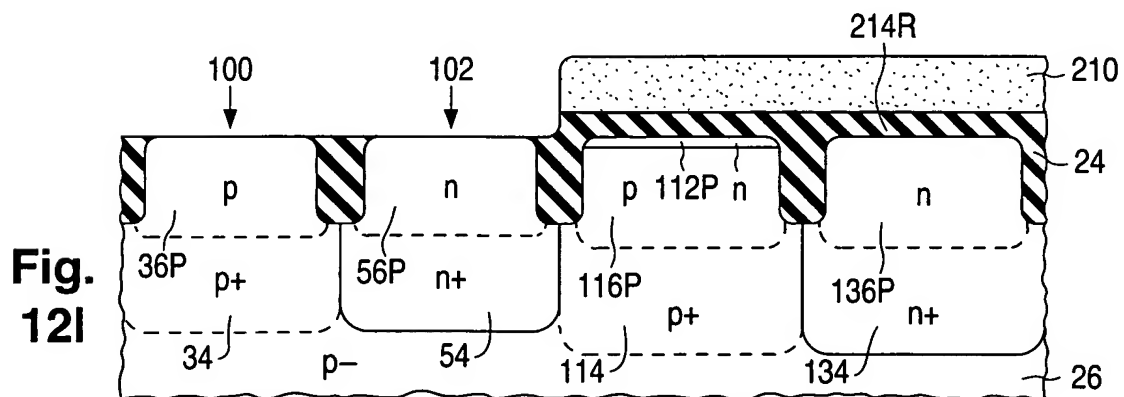
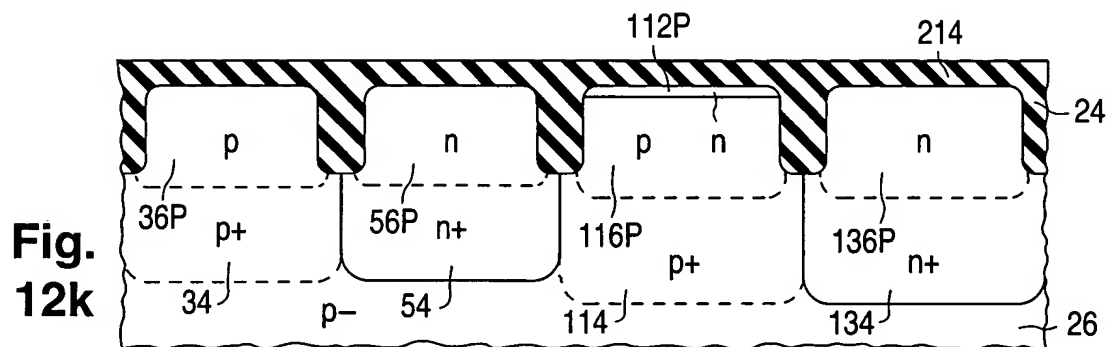
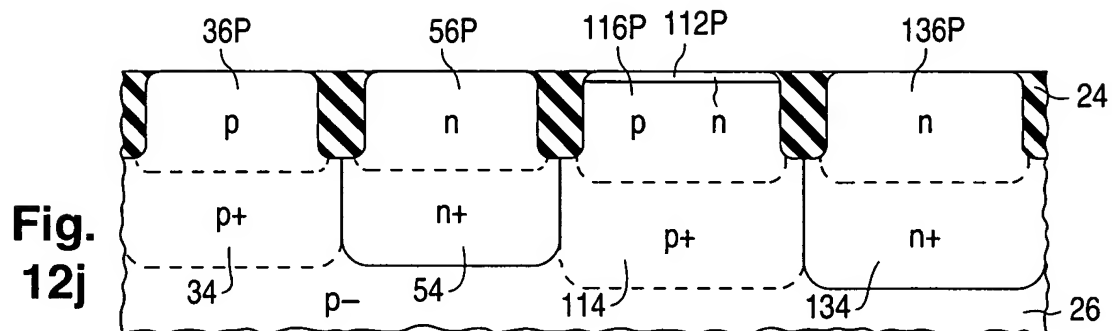




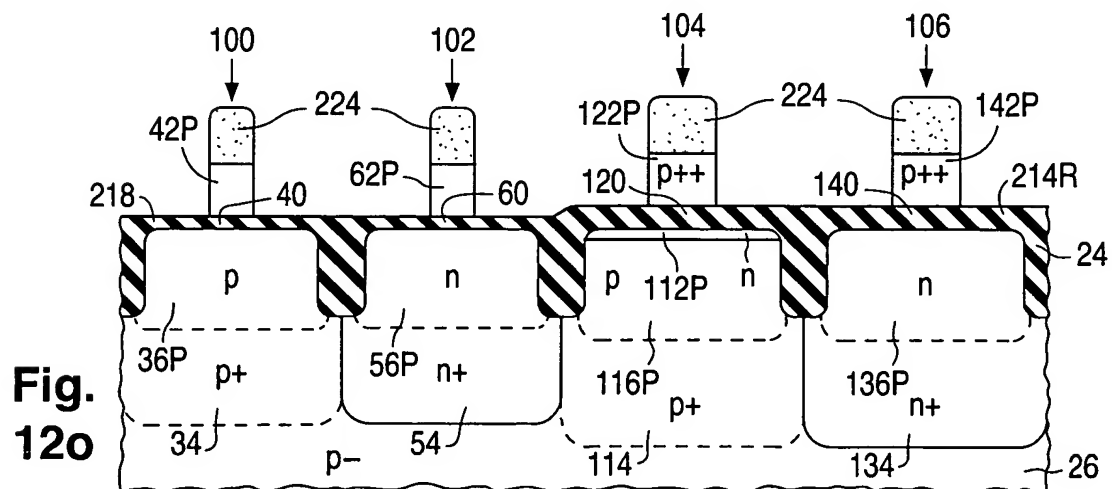
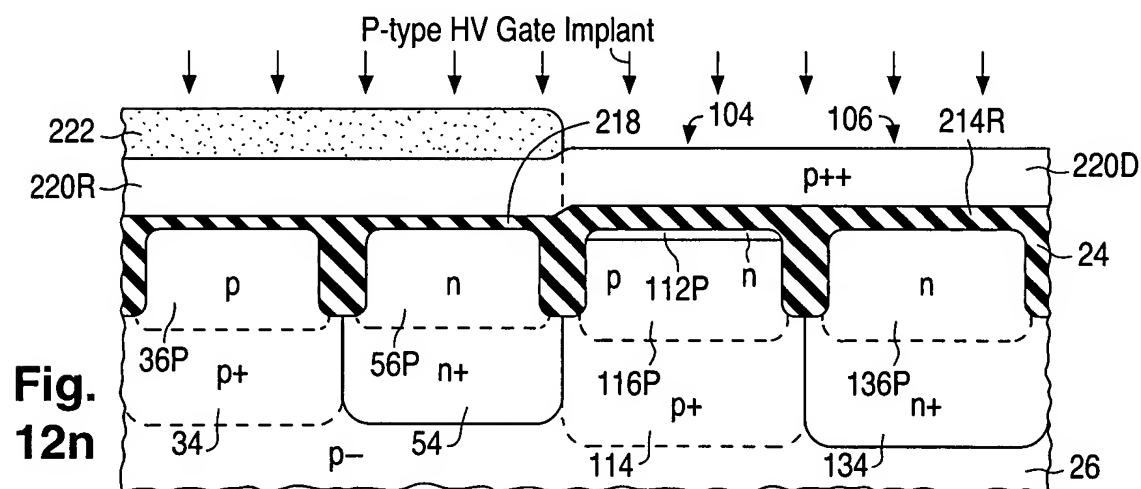
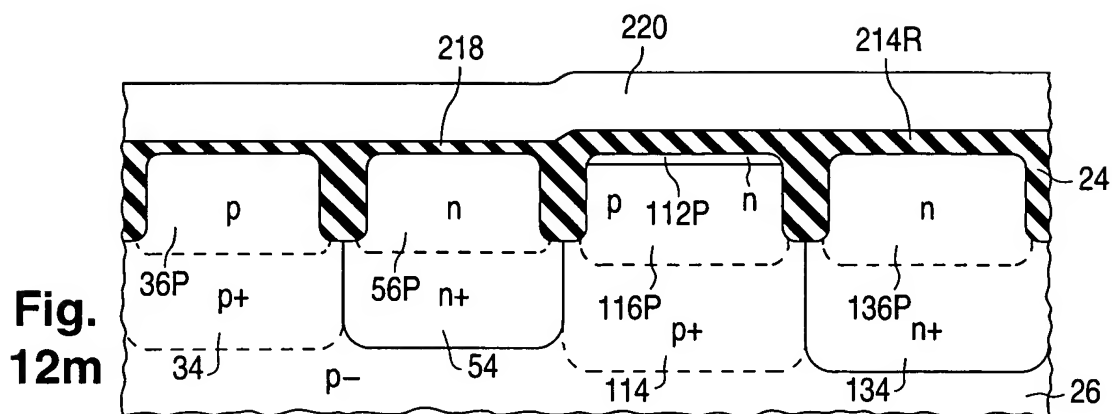
9/24



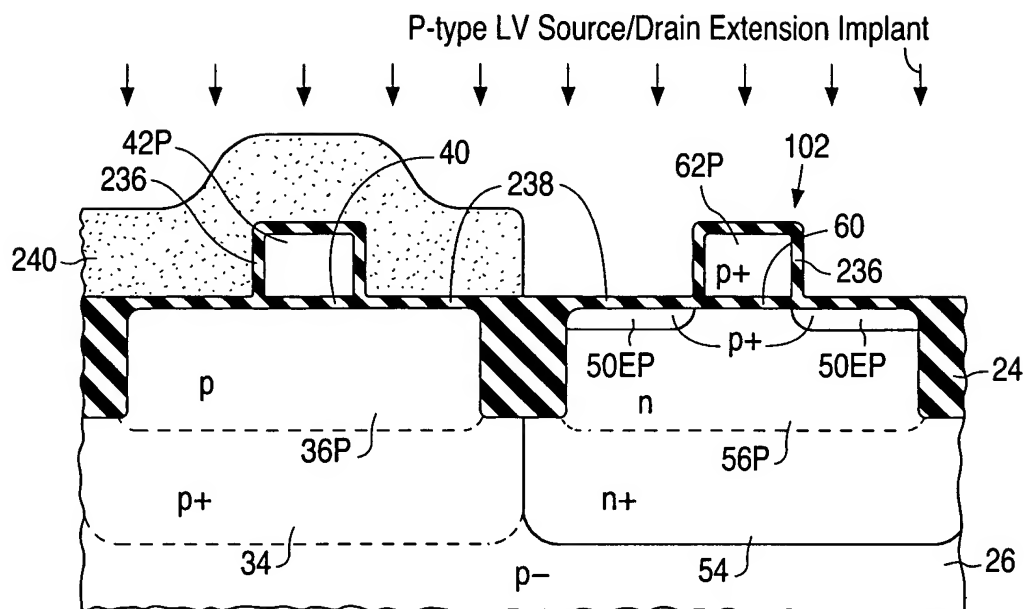
10/24



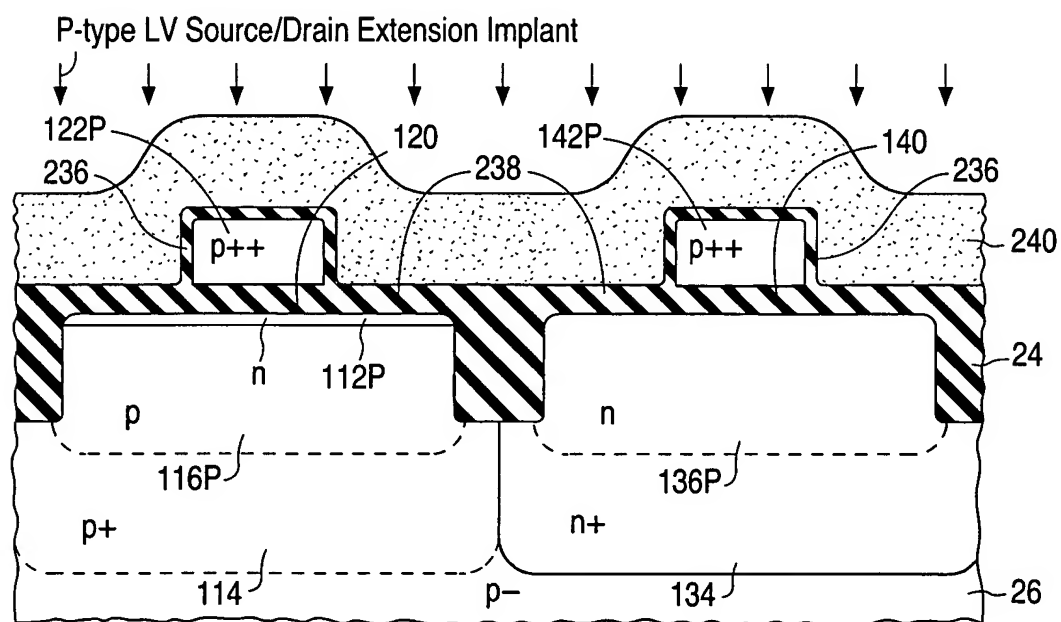
11/24



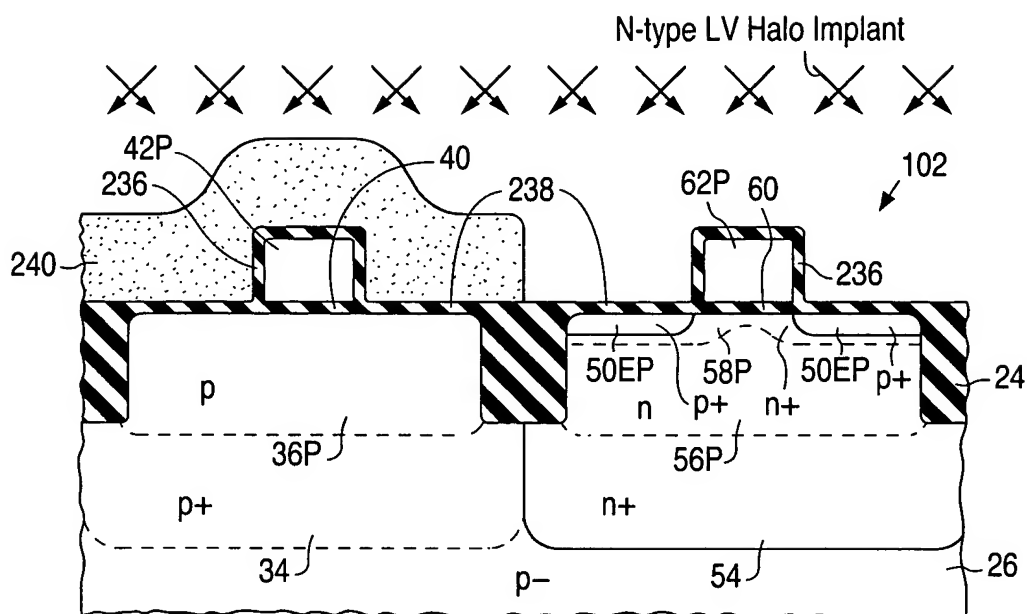
12/24



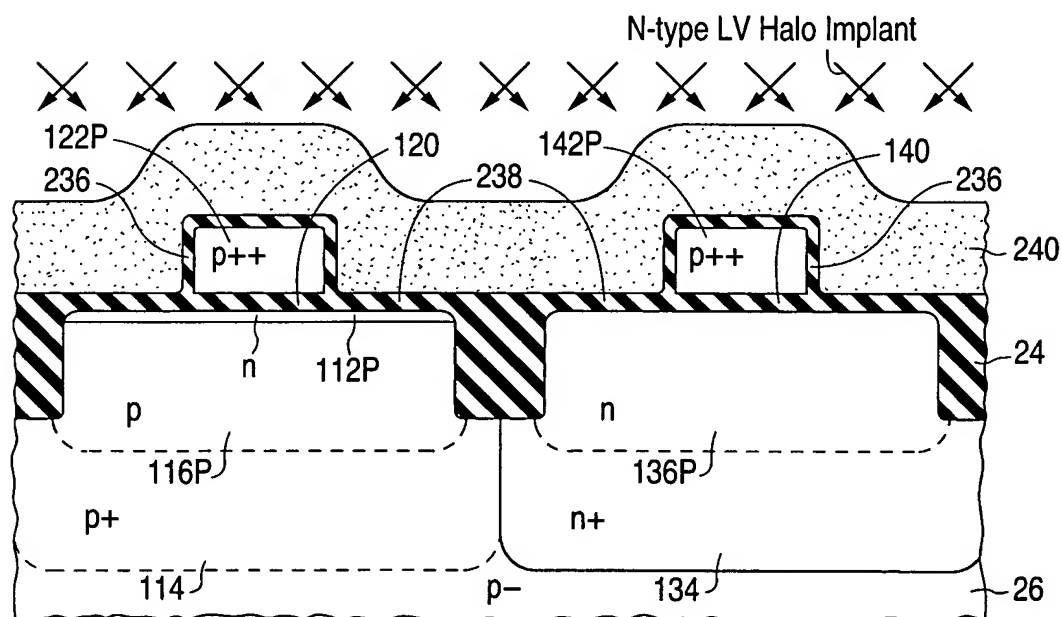
**Fig. 12p.1**



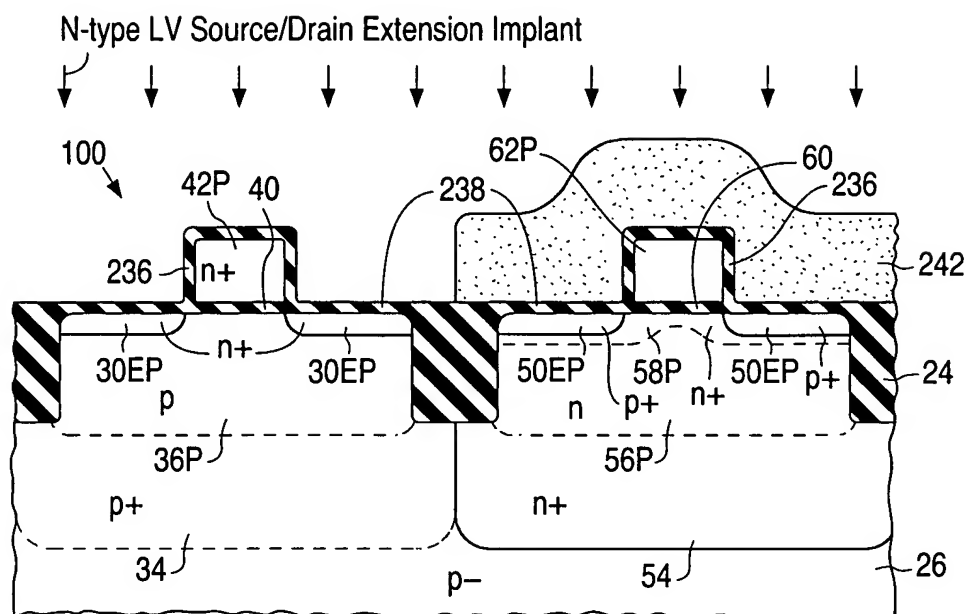
**Fig. 12p.2**



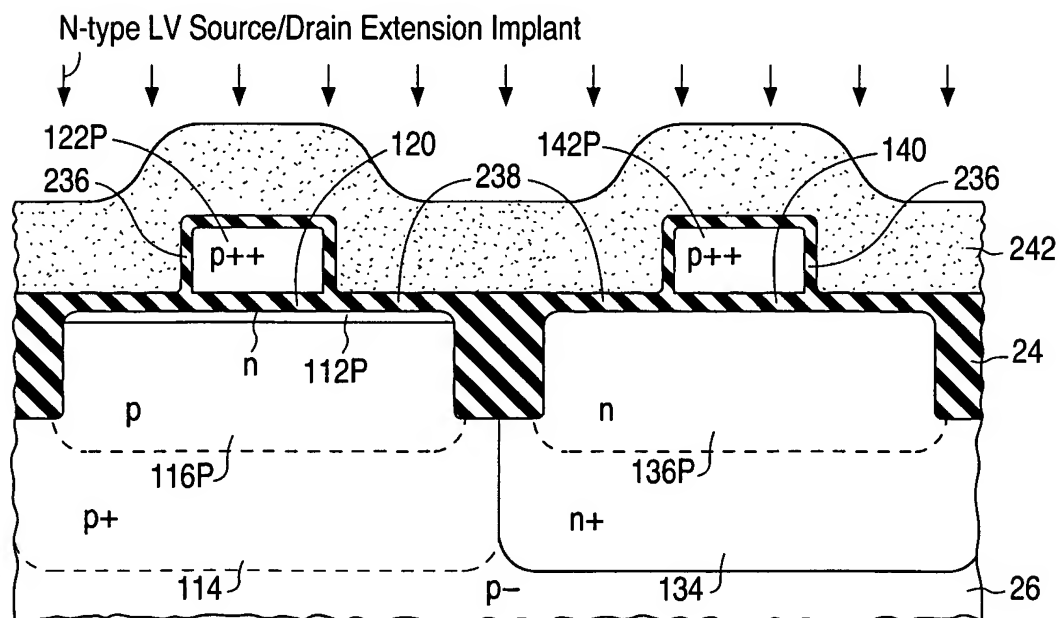
**Fig. 12q.1**



**Fig. 12q.2**

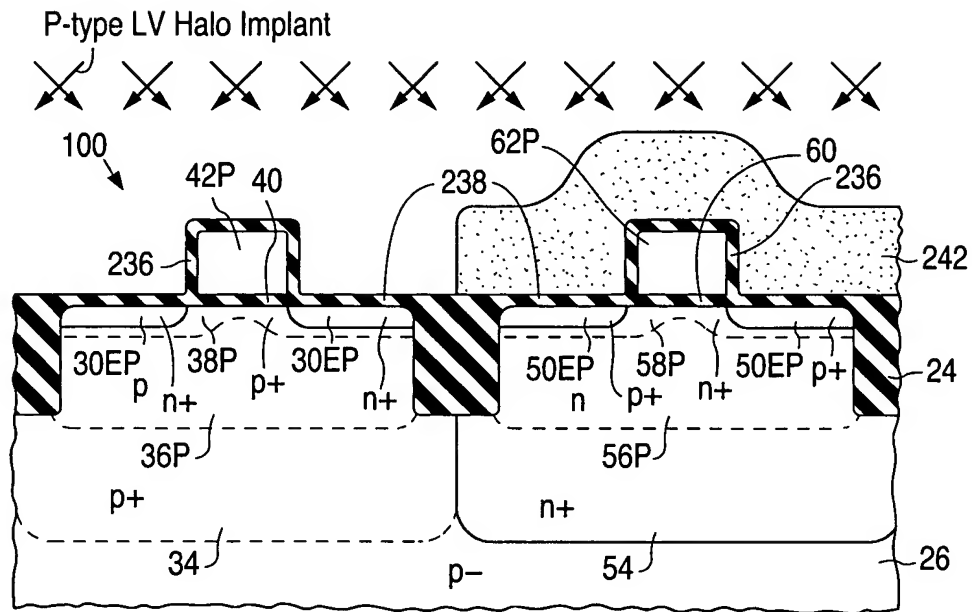
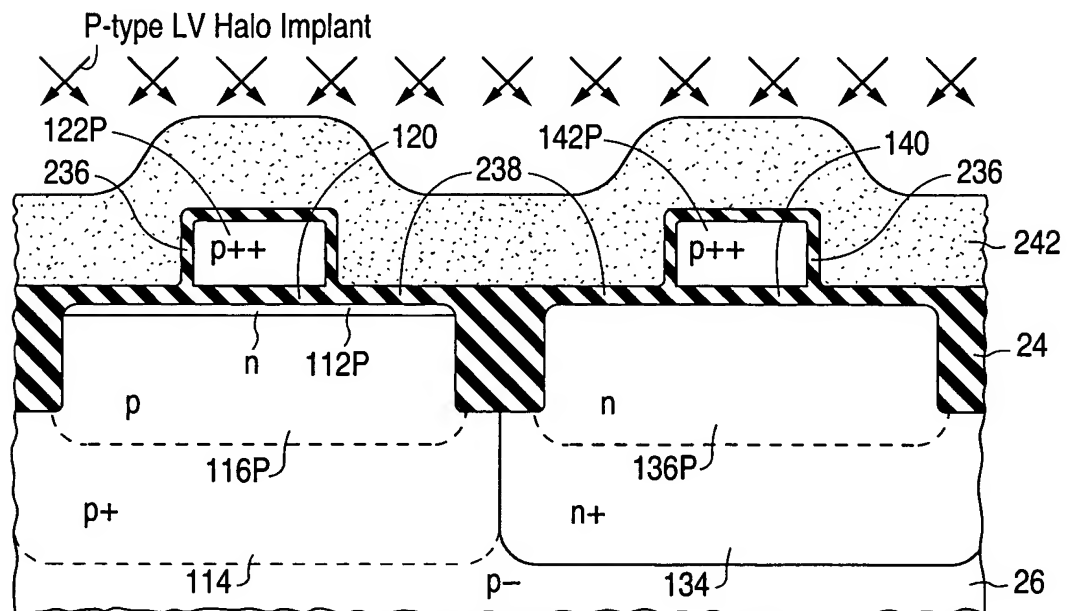


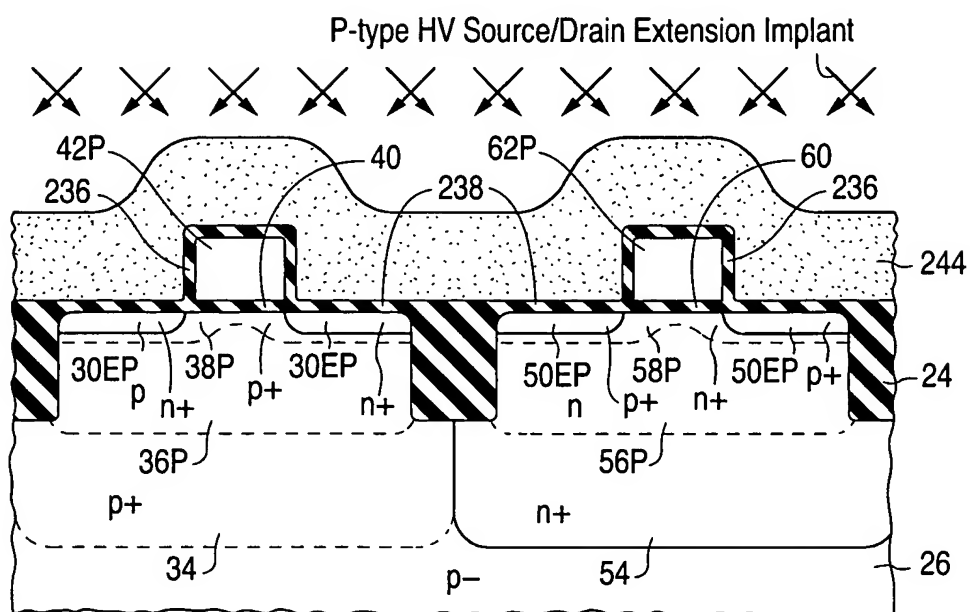
**Fig. 12r.1**



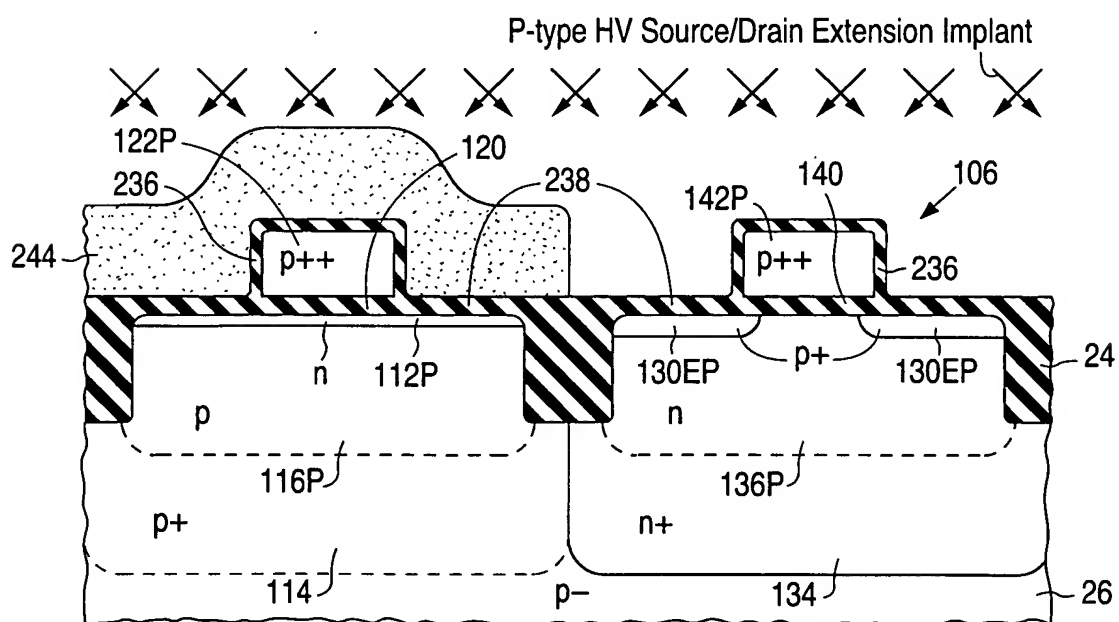
**Fig. 12r.2**

15/24

**Fig. 12s.1****Fig. 12s.2**

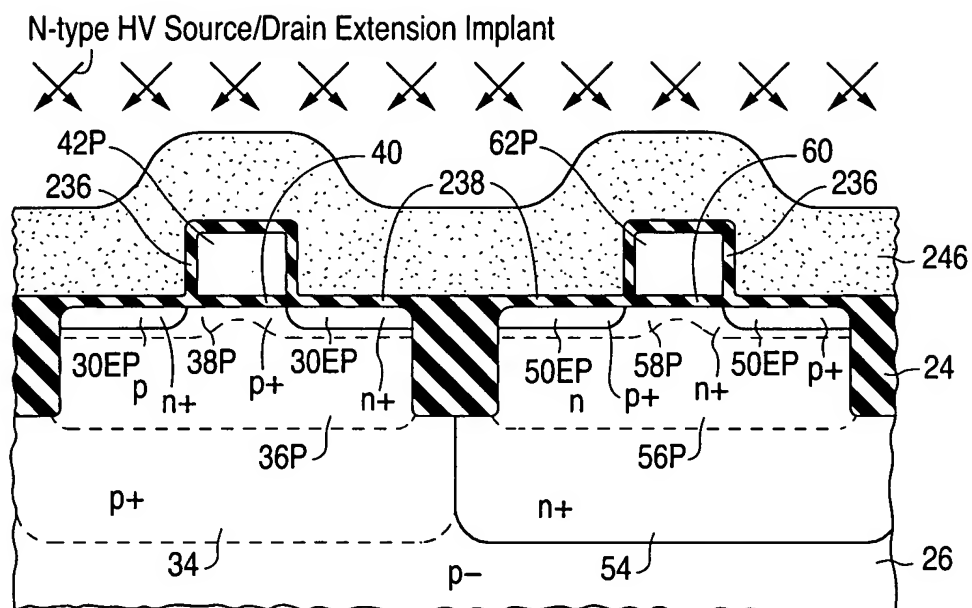


**Fig. 12t.1**

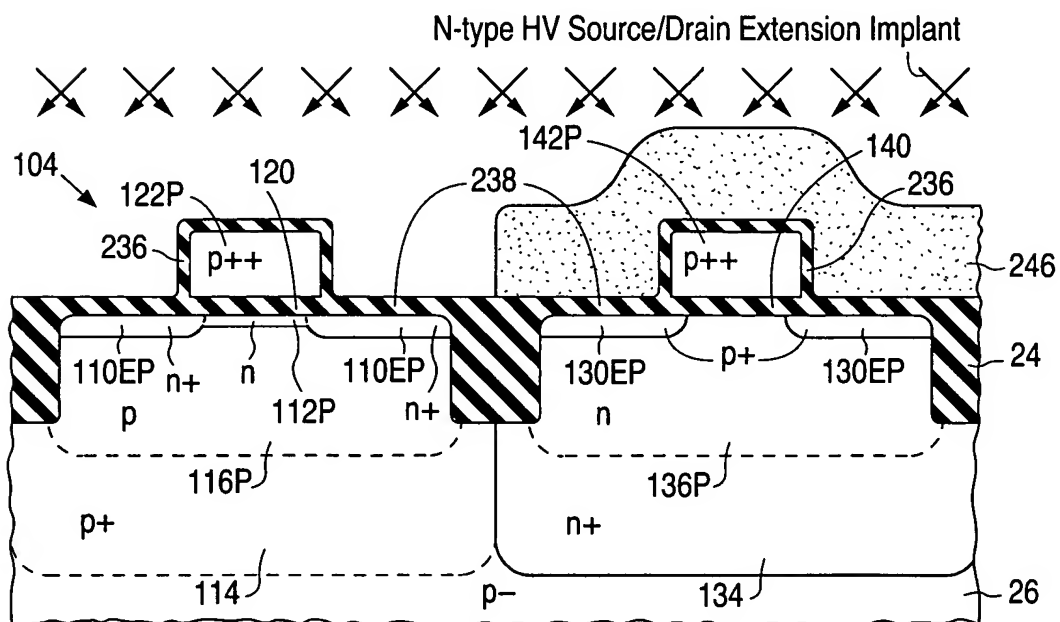


**Fig. 12t.2**

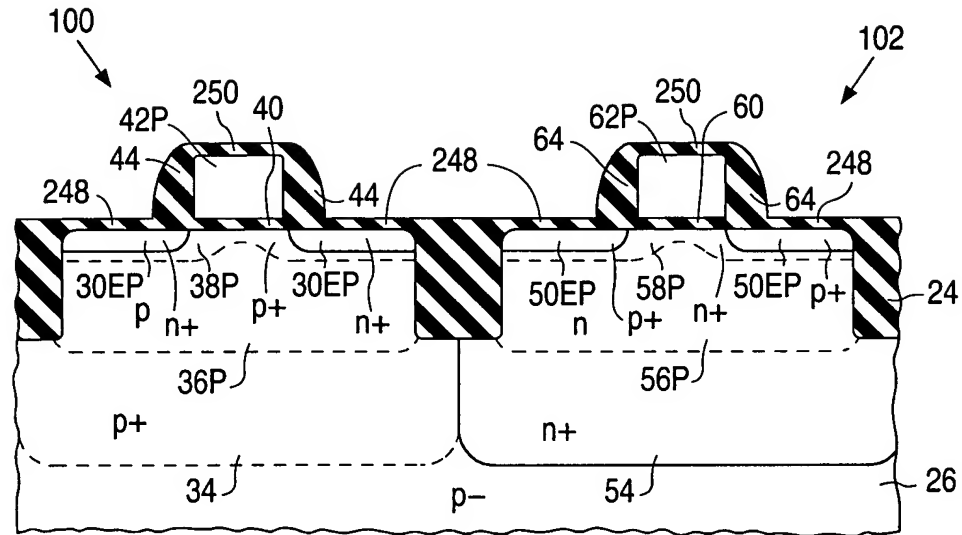




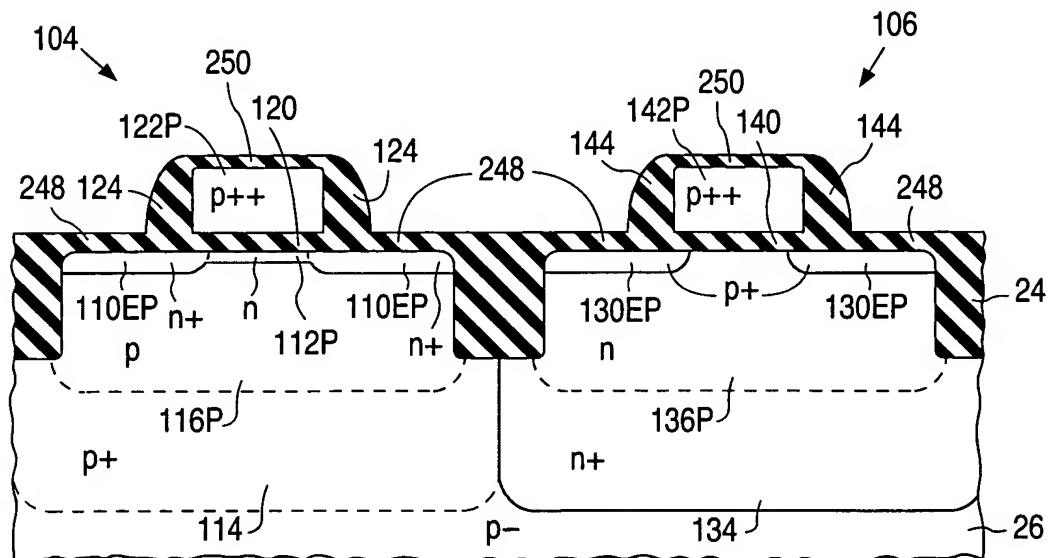
**Fig. 12u.1**



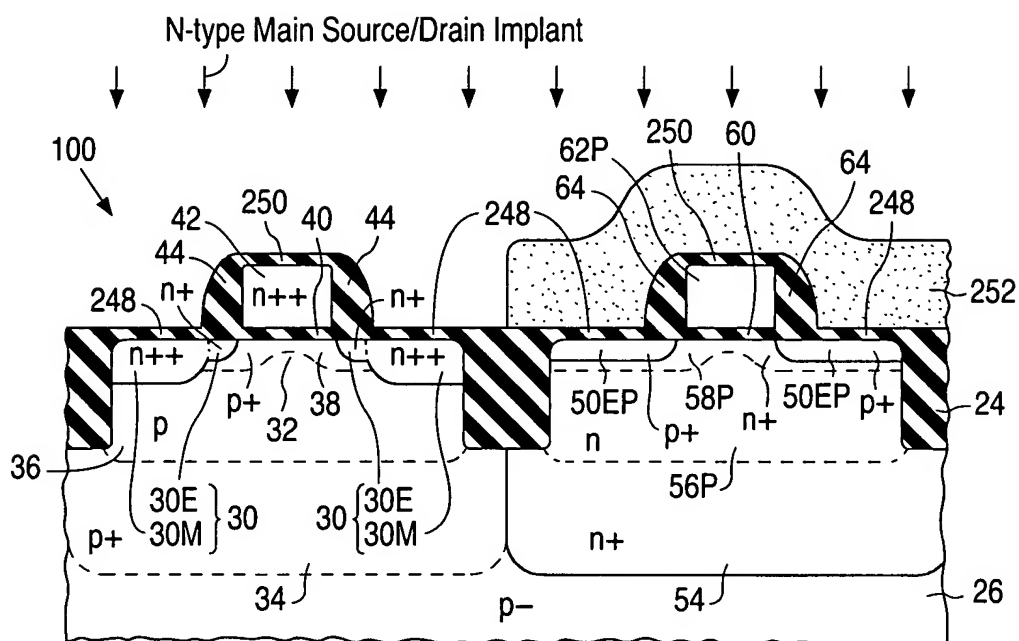
**Fig. 12u.2**



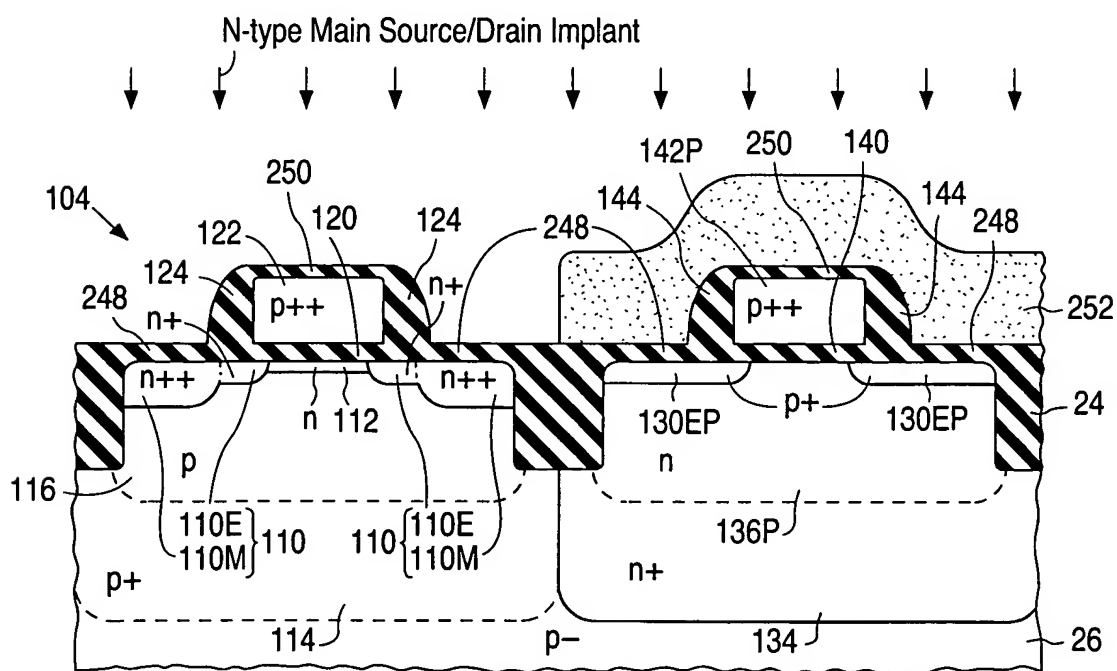
**Fig. 12v.1**



**Fig. 12v.2**

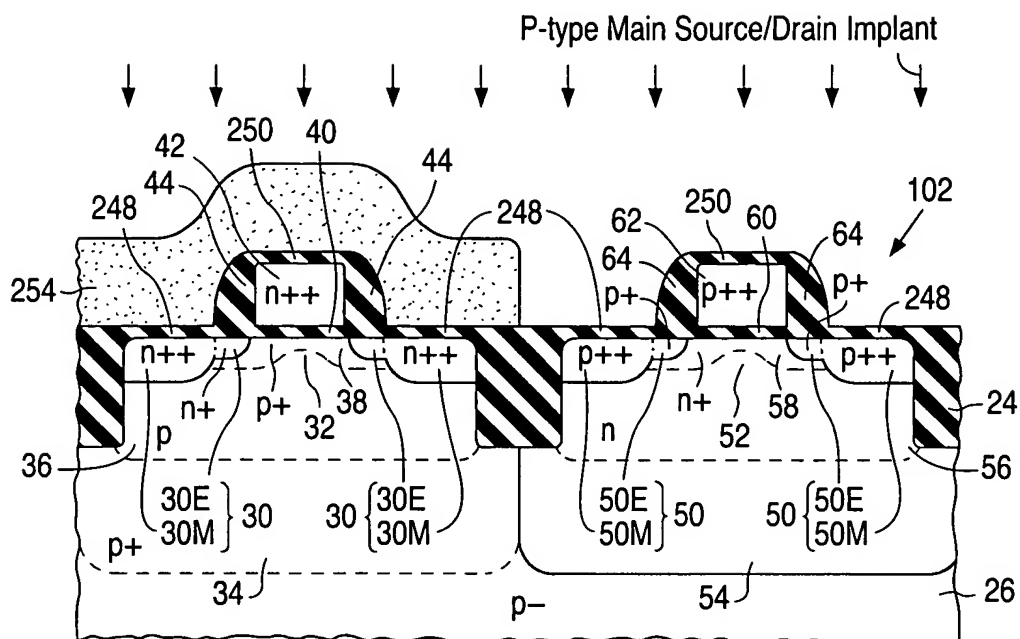
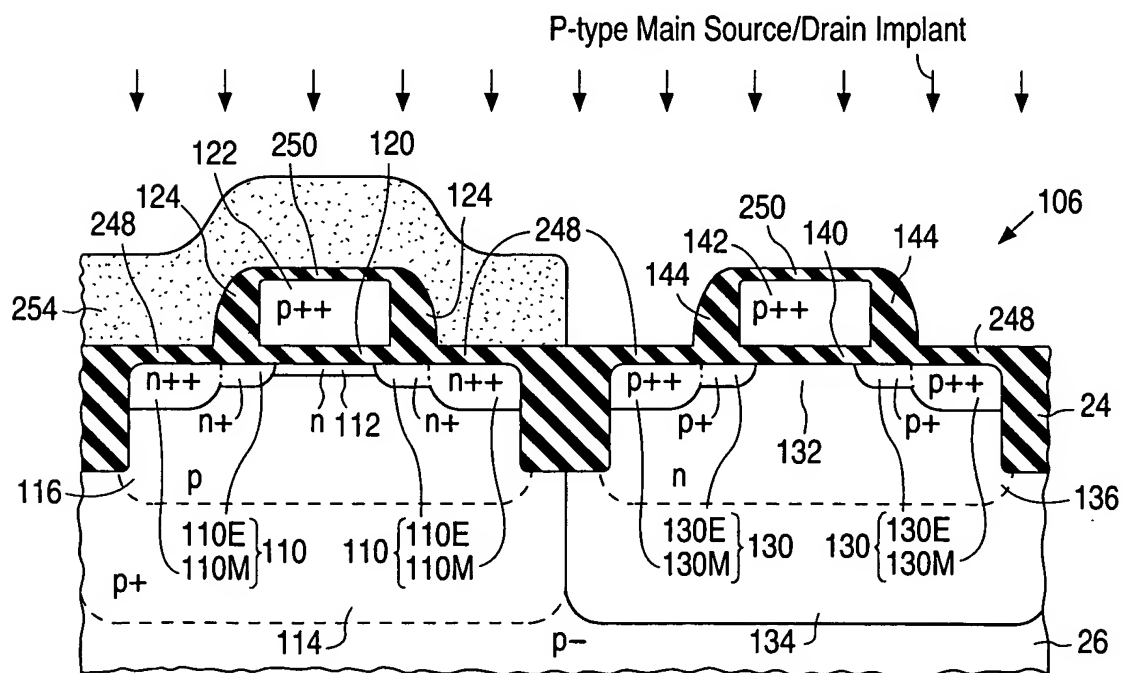


**Fig. 12w.1**

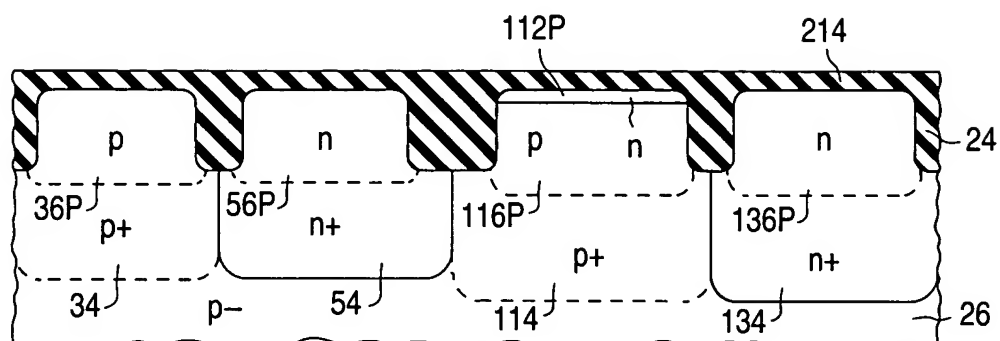
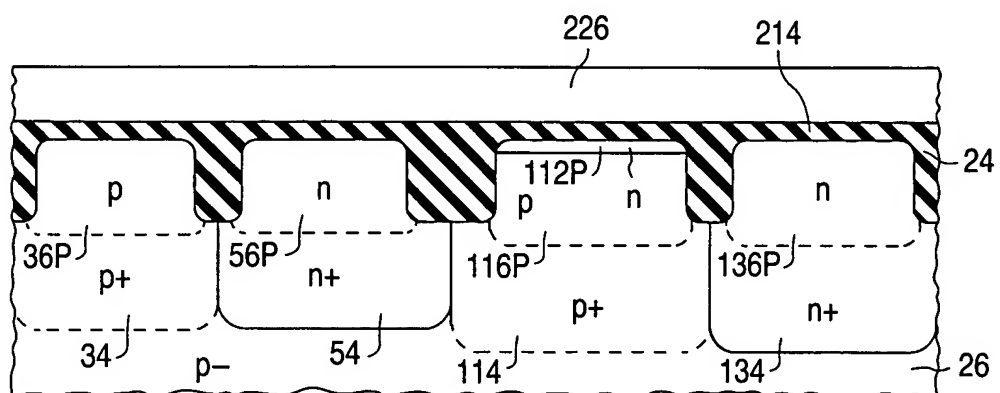
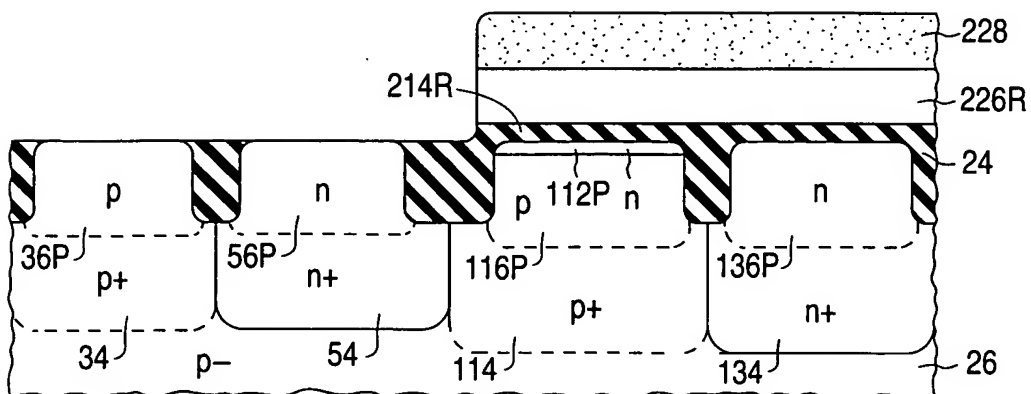


**Fig. 12w.2**

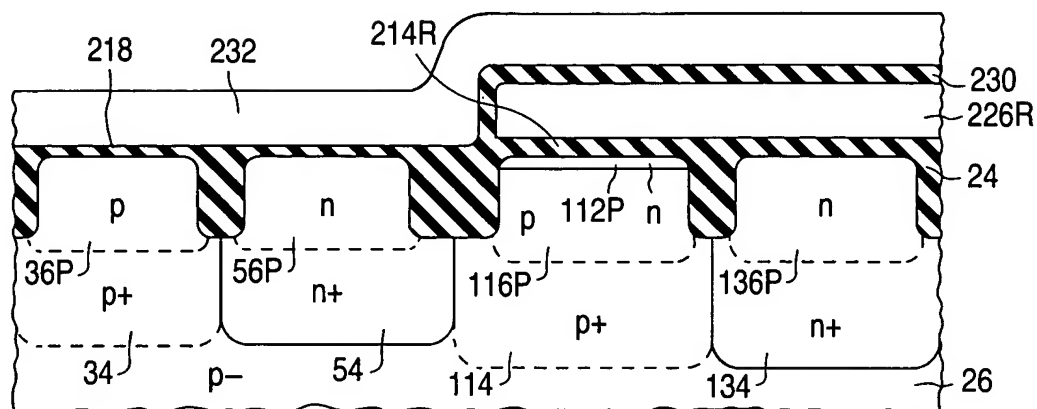
20/24

**Fig. 12x.1****Fig. 12x.2**

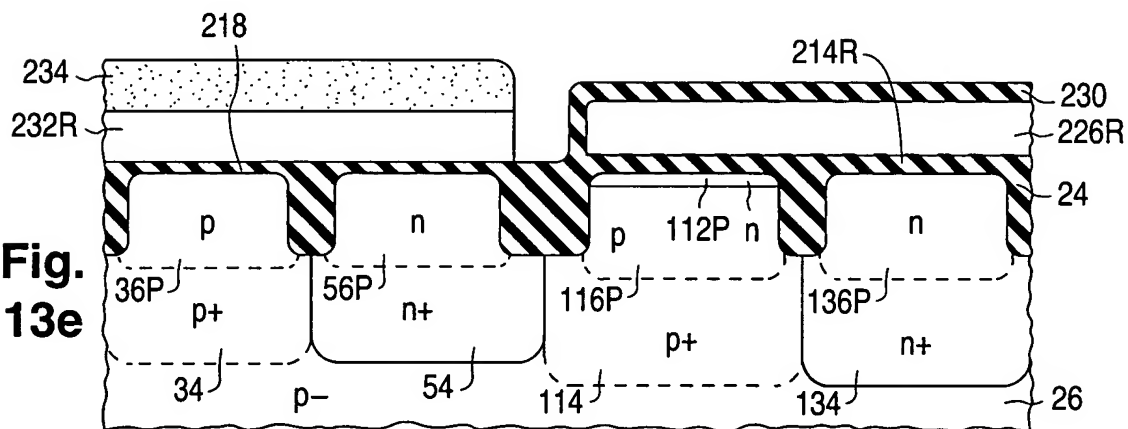
21/24

**Fig.  
13a****Fig.  
13b****Fig.  
13c**

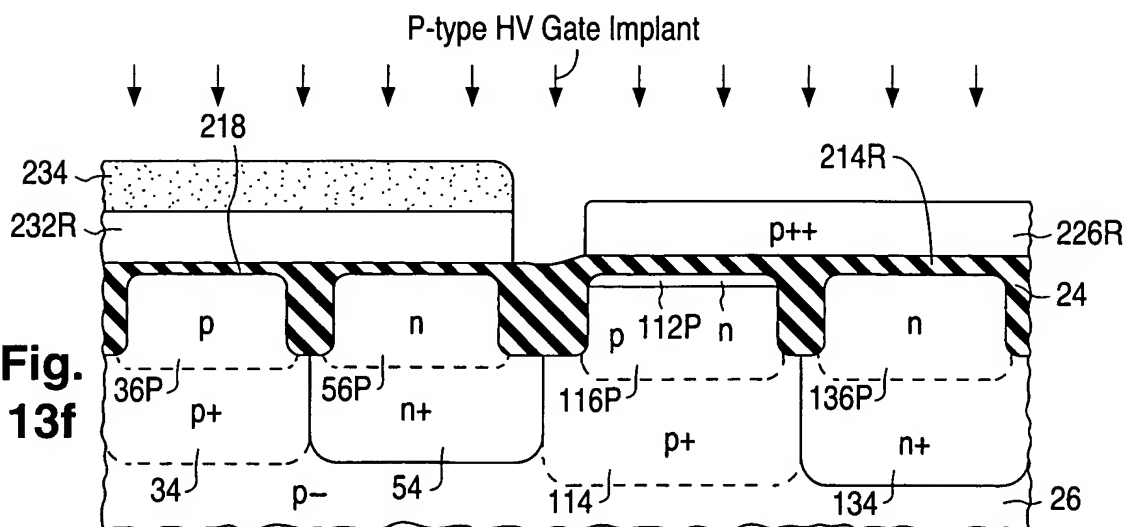
**Fig.  
13d**



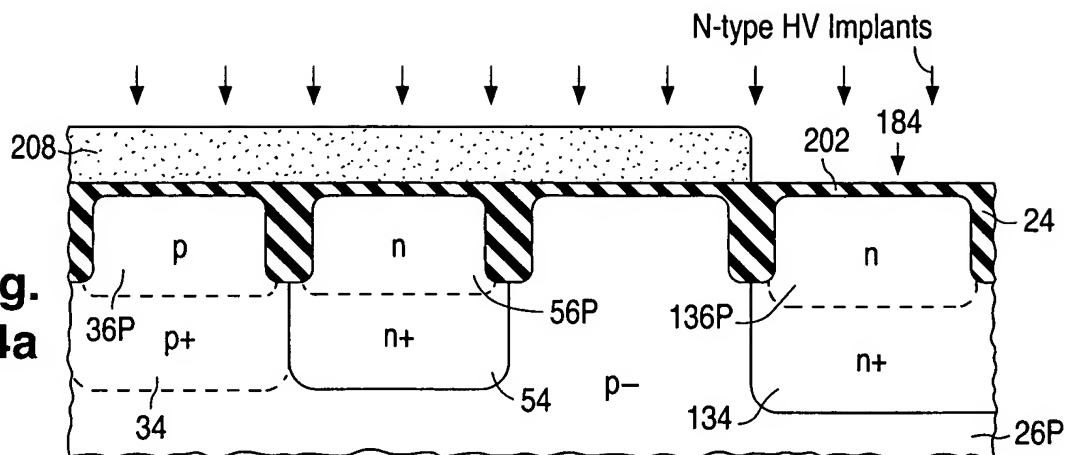
**Fig.  
13e**



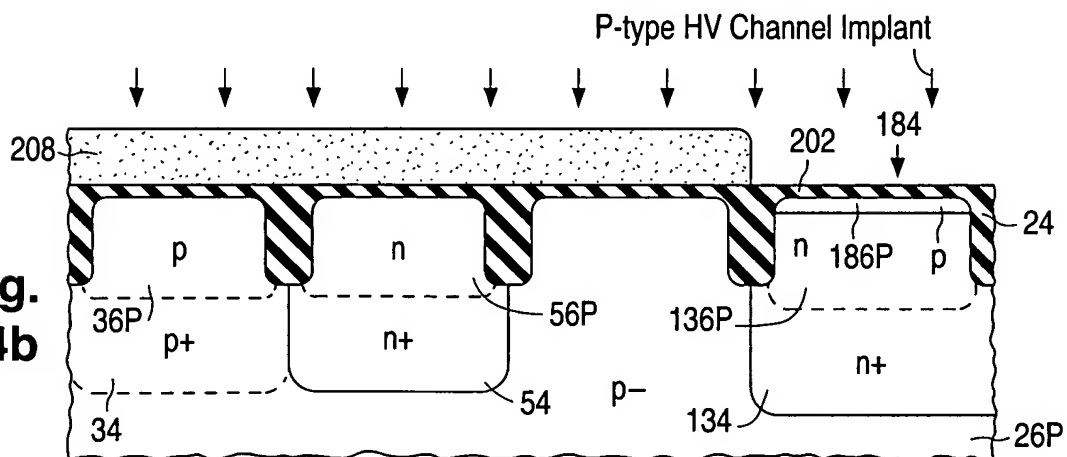
**Fig.  
13f**



**Fig.  
14a**



**Fig.  
14b**



**Fig.  
14c**

